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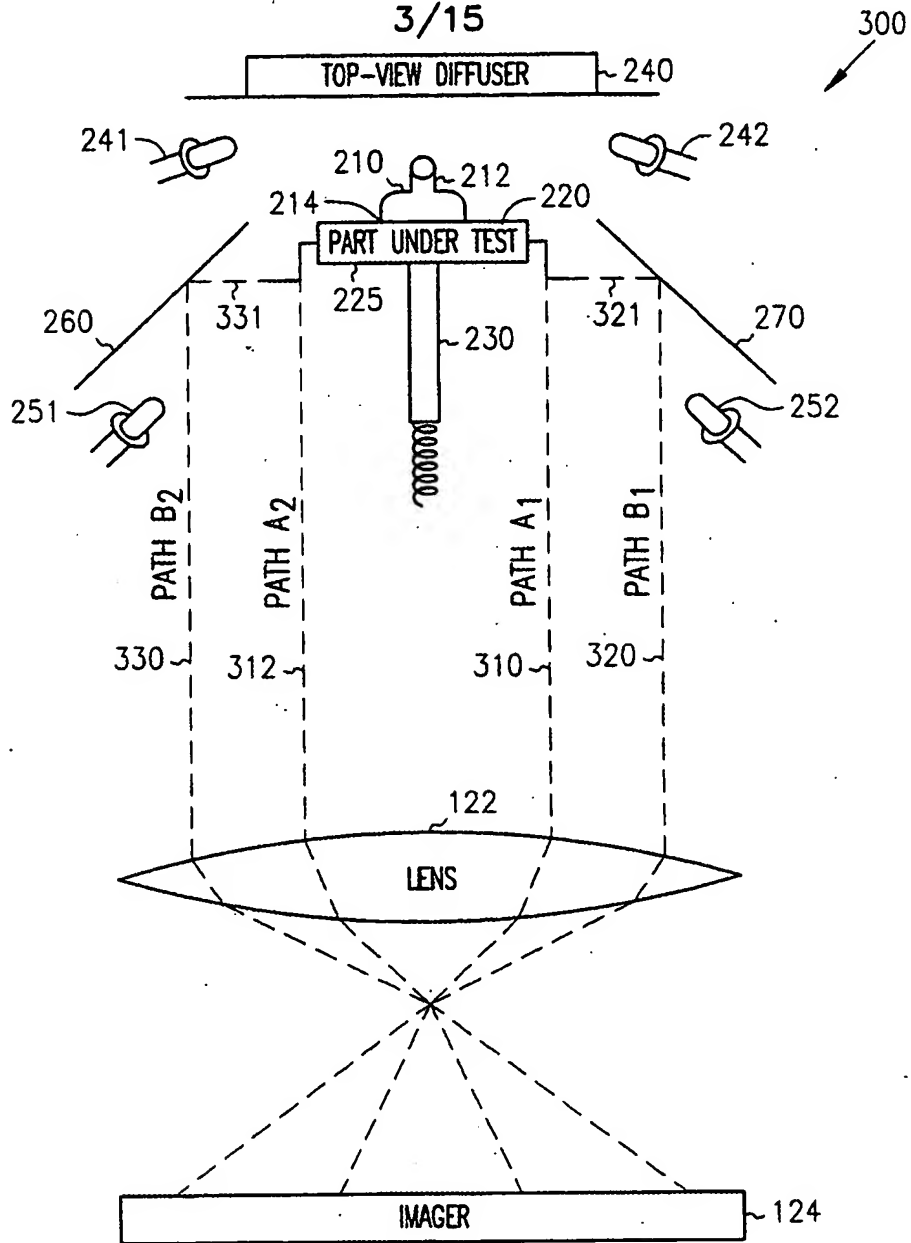


FIG. 3A

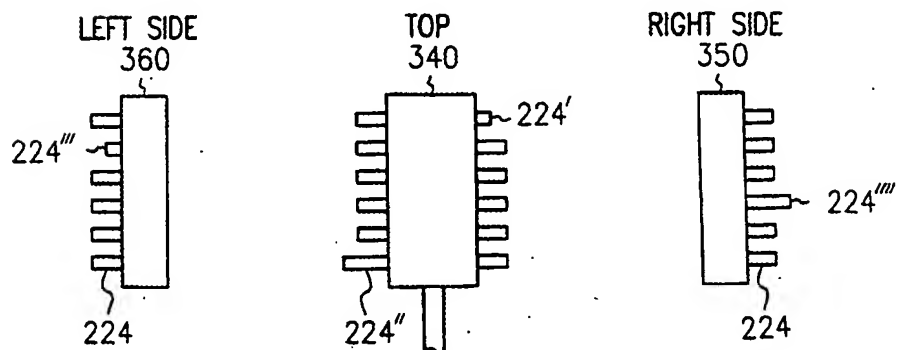


FIG. 3B



TITLE: CO-PLANARITY AND TOP-DOWN EXAMINATION METHOD AND OPTICAL MODULE FOR ELECTRONIC LEADED COMPONENTS

INVENTOR: BRADLEY L. QUIST  
SERIAL NO.: 10/099,684

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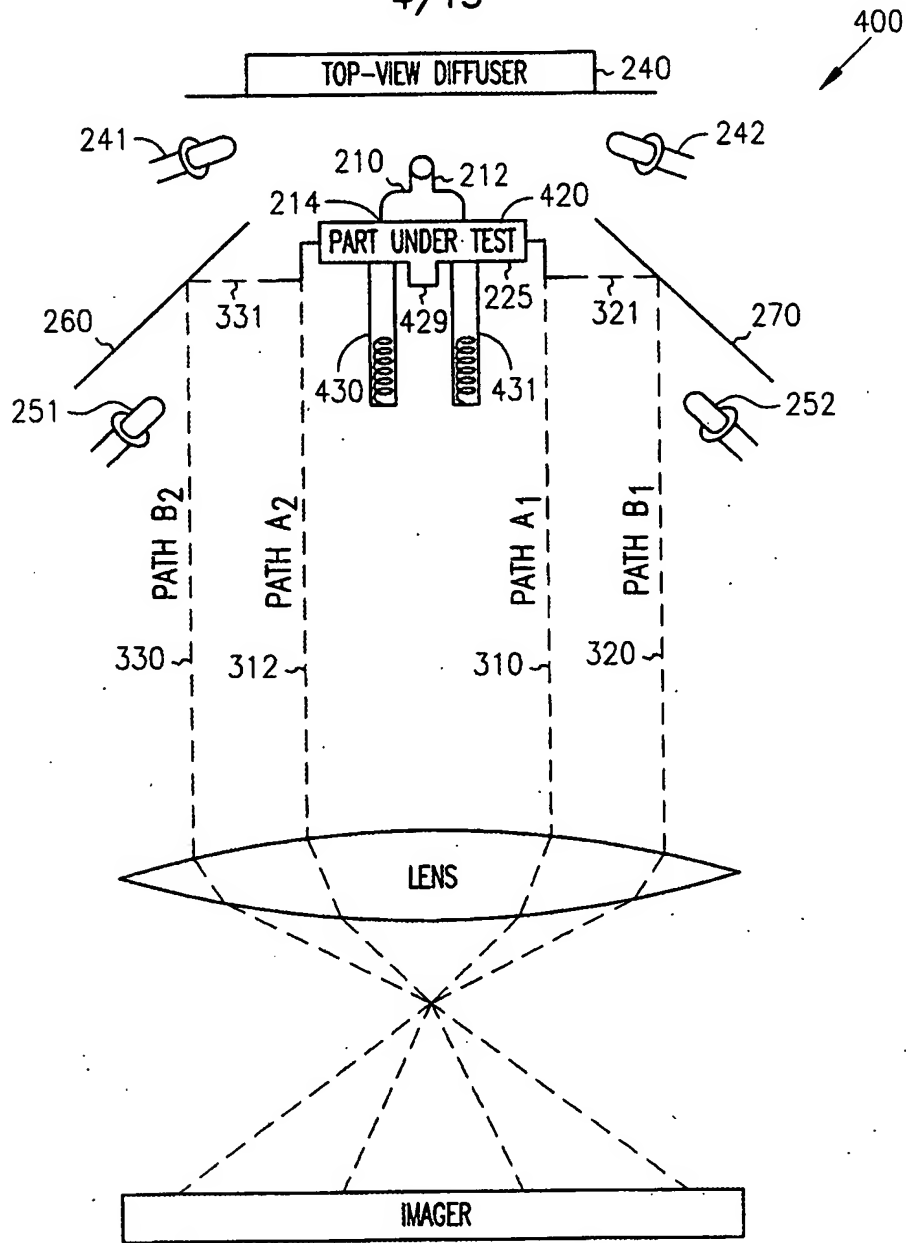


FIG. 4A

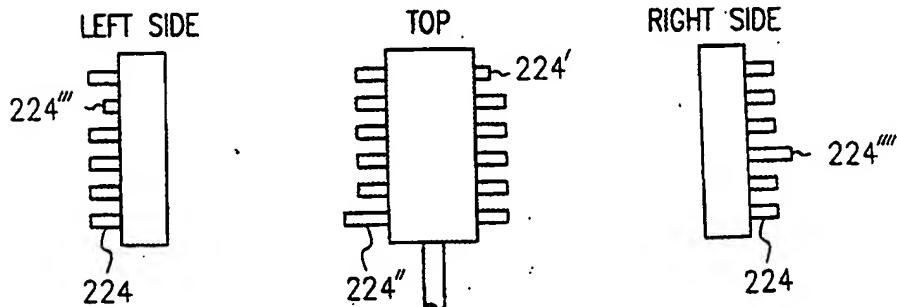


FIG. 4B



TITLE: CO-PLANARITY AND TOP-DOWN EXAMINATION METHOD AND OPTICAL MODULE FOR ELECTRONIC LEADED COMPONENTS

INVENTOR: BRADLEY L. QUIST  
SERIAL NO.: 10/099,684

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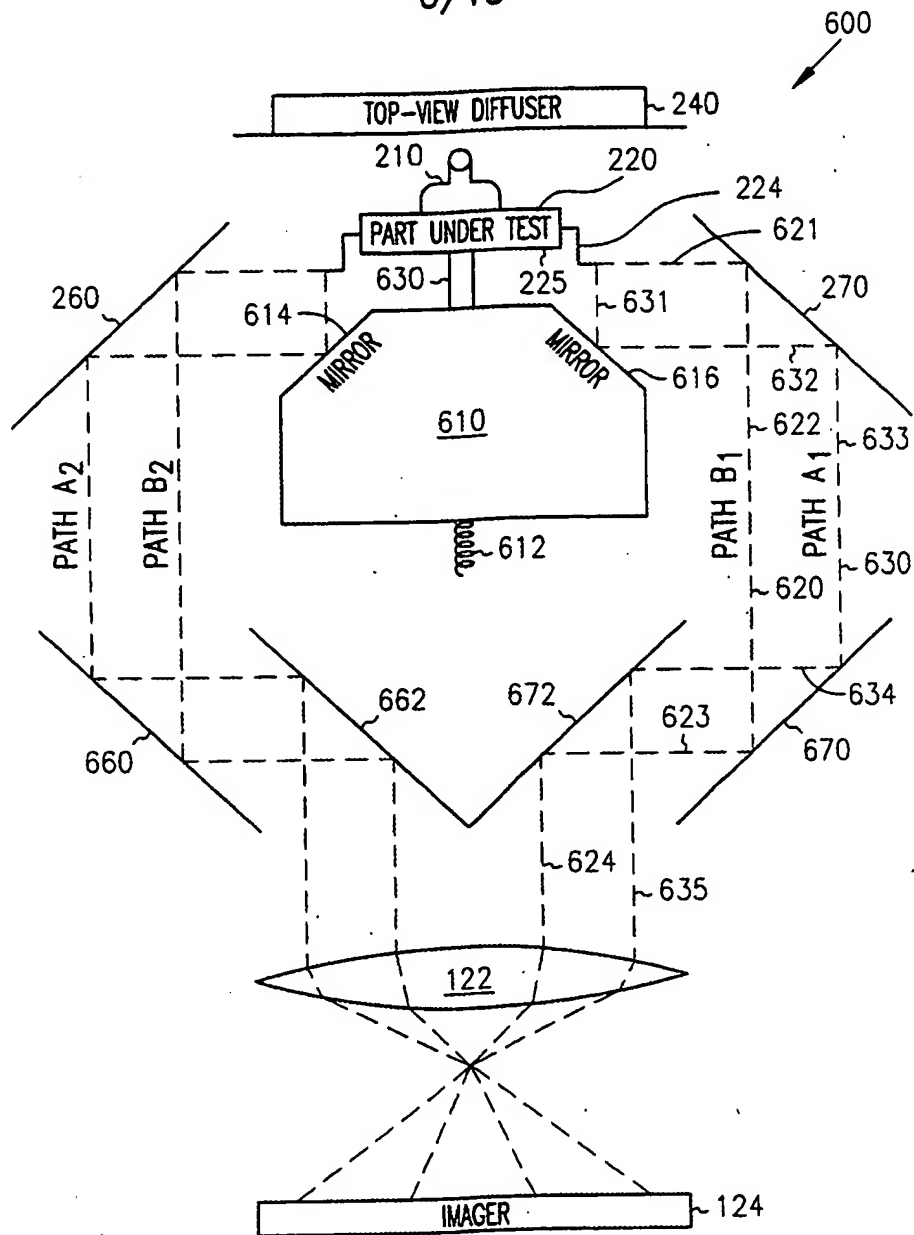


FIG. 6A

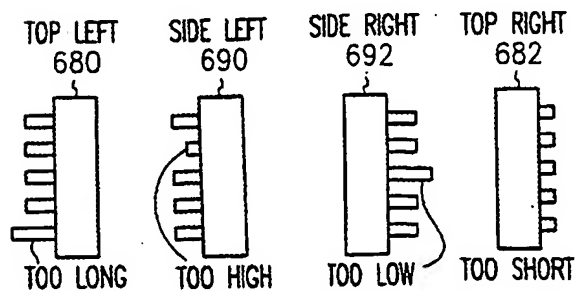


FIG. 6B

TITLE: CO-PLANARITY AND TOP-DOWN EXAMINATION METHOD AND OPTICAL MODULE FOR ELECTRONIC LEADED COMPONENTS

INVENTOR: BRADLEY L. QUIST

SERIAL NO.: 10/099,684

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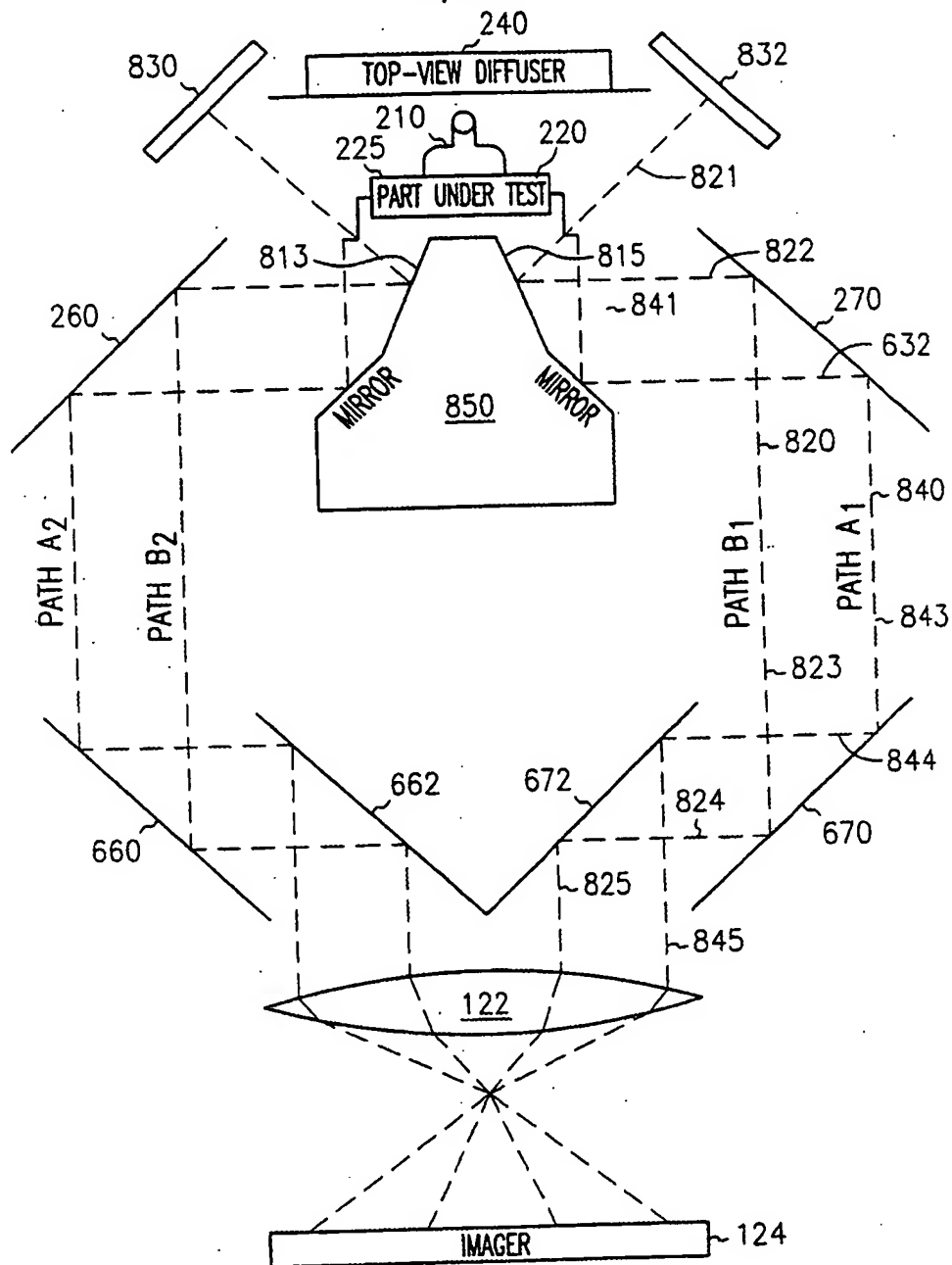


FIG. 8A

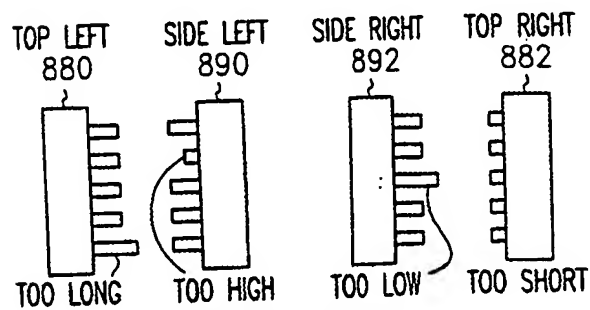


FIG. 8B



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TITLE: CO-PLANARITY AND TOP-DOWN EXAMINATION METHOD AND OPTICAL MODULE FOR ELECTRONIC LEADED COMPONENTS

INVENTOR: BRADLEY L. QUIST

SERIAL NO.: 10/099,684

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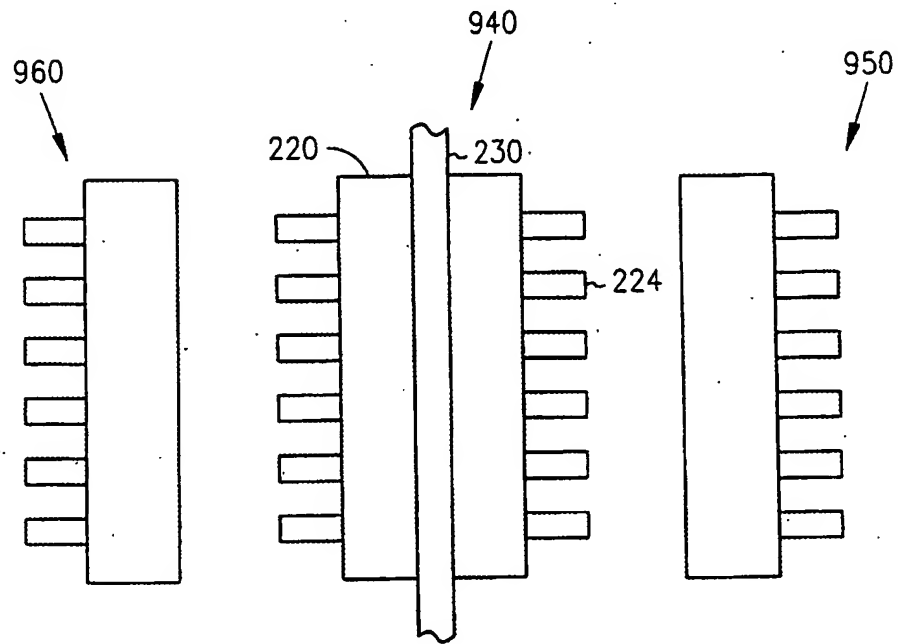


FIG. 9